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1. AN **EMBEDDED PROCESSOR** BASED SOC **TEST** PLATFORM

   Besides, the constrained **I/O** bandwidth may affect the **test** performance and increase
   test time. An alternative .... The **input/output** signals drawn in white are system
   signals, ... **Controller**. In this version of **test** platform, there are three ... **test** platform.
   Note that the **embedded** memory and **processor** ...
   ieeexplore.ieee.org/iel5/9898/31469/01465254.pdf

2. **EMBEDDED** DEVICE PROGRAM **DEBUG** CONTROL - Patent application ...

   An **embedded processor** system includes an integrated development .... not usually
   available during a typical **embedded controller** software **debug** session, ... for reboots
   and retrys during **debug** as well as for board verification and **test**. ... an **input/output**
   stream through the command queue so that a **debug** session can ...
   www.faqs.org/patents/app/20090019270 - Cached

3. [PDF] **Embedded Processor** Based Built-In Self-**Test** and Diagnosis of Logic ...

   File Format: PDF/Adobe Acrobat - Quick View
   by D Milton - Cited by 5 - Related articles
   containing two 4-**input** look-up tables (LUTs) and two flip-flops along with other ...
   output from the RUT to the left and the other to the right of the ORA. ... ferred to as a
   test session. Since the **embedded processor** core is used for ...
   citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.94.3451&rep...

4. Soft Core **Embedded Processor** Based Built-In Self-**Test** of FPGAs

   by BF Dutton - Cited by 1 - Related articles
   This **embedded processor** based BIST approach achieved a total **test** time speed- ...
   CLBs and **I/O** tiles, or 4 rows of block RAMs and DSP tiles. ... procedures can also be
   performed by the **embedded** BIST **controller**, ... Software **Debug** download ... "Built-
   In Self-**Test** of Programmable **Input/Output** Tiles in ...
   www.eng.auburn.edu/~strouce/class/bist/DFT09embedFPGA.pdf

5. Canadian Inventors Develop Multithread **Embedded Processor** with ...

Canadian Inventors Develop Multithread **Embedded Processor** with **Input**, **Output** Capability ... find US Fed News Service, Including US State News articles. ALEXANDRIA, Va., Jan. ... Article: California Inventors Develop **Embedded Processor** . ... The **test** ... information about US Fed News federal patent awards . ... www.highbeam.com › ... › Wednesday, January 16 - Cached

6. Novas Extends Industry-Standard **Debug** Platform for **Embedded** ...

... structures such as buses, interfaces and **IO** components. ... The emergence of system-level techniques for both **embedded processor** ... model to **output debug** information into their transaction-based products. ... Previous Article: Providian Financial Announces Preliminary Results of **I/O** Strip Accounting Review. ... www.thefreelibrary.com/Novas+Extends+Industry-Standard+**Debug**+Platform+for +**Embedded**...-a0129351281 - Cached

7. Tracer, tracer **embedded processor**, and method for operating the ...

The **processor** of claim 8, wherein the **debug** module is connected to an external JTAG debugger via the JTAG debugger interface and a JTAG **debug output** bus, ... microprocessor **test** mode circuit, scan path)714/31, Additional **processor** for ... www.patentstorm.us/patents/7444553/claims.html

8. What simple method can I use to **debug** an **embedded processor** ...

If you have multiple **output** lines available, you can use a 7 segment LED ... your system the use of LEDs connected to spare **I/O** pins will be helpful. These can also be used as the **input** to a digital storage oscilloscope (DSO) or logic analyzer. .... Minimum configuration to run **embedded** Linux on an ARM **processor**? ... stackoverflow.com/.../what-simple-method-can-i-use-to-**debug**-an-**embedded**- **processor**-without-serial-port-o - Cached

9. AN 351: Simulating Nios II **Processor** Designs

solution supplied with the **processor**. Nios o II **embedded processor** designs support a broad ... You can further **debug** the hardware components that interact with the **processor** .... or both **input** and **output** ports, for simulation in SOPC Builder. .... The interface between the SDRAM **controller** and SDRAM device(s) ... www.altera.com/literature/an/an351.pdf - Similar

10. Selective-Run Built-In Self-**Test** Using an **Embedded Processor**

by S Hwang - 2002 - Cited by 4 - Related articles ded **processor** can access **I/O** terminals of other **embedded** cores in **test** mode via an already ... **Input** boundary scan. **Output** boundary scan. Control. Register ... portal.acm.org/ft_gateway.cfm?id=505333&type=pdf

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